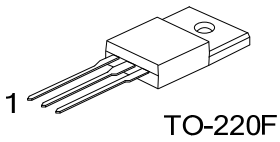
9 Amps, 500 Volts
N-CHANNEL POWER MOSFET



TO-220F

■ DESCRIPTION

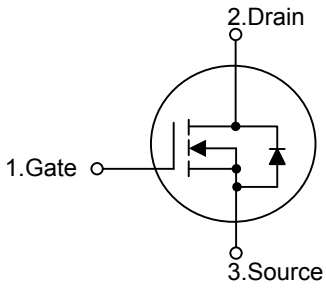
The UTC **9N50** is an N-channel mode power MOSFET using UTC's advanced technology to provide customers planar stripe and DMOS technology. This technology allows a minimum on-state resistance, superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

The UTC **9N50** is generally applied in high efficiency switch mode power supplies, active power factor correction and electronic lamp ballasts based on half bridge topology.

■ FEATURES

* 9A, 500V, $R_{DS(ON)}$=0.8Ω @ V_{GS}=10V
* High Switching Speed
* Improved dv/dt Capability
* 100% Avalanche Tested

■ SYMBOL



■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
9N50L-TF3-T	9N50G-TF3-T	TO-220F	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

9N50L-TF3-T (1)Packing Type (2)Package Type (3)Lead Free	(1) T: Tube (2) TF3: TO-220F (3) G: Halogen Free, L: Lead Free

■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise noted)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	500	V
Gate-Source Voltage		V_{GSS}	±30	V
Drain Current	Continuous (T_C=25°C)	I_D	9 *	A
	Pulsed (Note 1)	I_{DM}	36 *	A
Avalanche Current (Note 1)		I_{AR}	9	A
Avalanche Energy	Single Pulsed (Note 2)	E_{AS}	360	mJ
	Repetitive (Note 3)	E_{AR}	13.5	mJ
Peak Diode Recovery dv/dt (Note 3)		dv/dt	4.5	V/ns
Power Dissipation	T_C=25°C	P_D	44	W
	Derate above 25°C		0.35	W/°C
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-55~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

* Drain current limited by maximum junction temperature

■ THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	62.5	°C/W
Junction to Case	θ_{JC}	2.86	°C/W

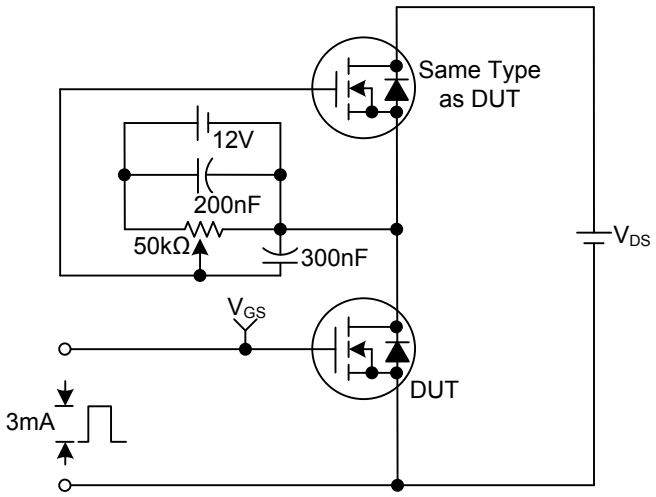
■ ELECTRICAL CHARACTERISTICS (T_C=25°C, unless otherwise noted)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	I_D=250μA, V_{GS}=0V	500			V
Drain-Source Leakage Current		I_{DSS}	V_{DS}=500V, V_{GS}=0V			1	μA
			V_{DS}=400V, T_C=125°C			10	
Gate- Source Leakage Current	Forward	I_{GSS}	V_{GS}=+30V, V_{DS}=0V			+100	nA
	Reverse		V_{GS}=-30V, V_{DS}=0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=250μA	2.0		4.0	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	V_{GS}=10V, I_D=4.5A		0.65	0.8	Ω
DYNAMIC PARAMETERS							
Input Capacitance		C_{ISS}	V_{GS}=0V, V_{DS}=25V, f=1.0MHz		790	1030	pF
Output Capacitance		C_{OSS}			130	170	pF
Reverse Transfer Capacitance		C_{RSS}			24	30	pF
SWITCHING PARAMETERS							
Total Gate Charge		Q_G	V_{GS}=10V, V_{DS}=400V, I_D=9A (Note 4, 5)		28	35	nC
Gate to Source Charge		Q_{GS}			4		nC
Gate to Drain Charge		Q_{GD}			15		nC
Turn-ON Delay Time		$t_{D(ON)}$	V_{DD}=250V, I_D=9A, R_G=25Ω (Note 4, 5)		18	45	ns
Rise Time		t_R			65	140	ns
Turn-OFF Delay Time		$t_{D(OFF)}$			93	195	ns
Fall-Time		t_F			64	125	ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I_S				9	A
Maximum Body-Diode Pulsed Current		I_{SM}				36	A
Drain-Source Diode Forward Voltage		V_{SD}	I_S=9A, V_{GS}=0V			1.4	V
Body Diode Reverse Recovery Time		t_{RR}	I_S=9A, V_{GS}=0V, dI_F/dt=100A/μs (Note 4)		335		ns
Body Diode Reverse Recovery Charge		Q_{RR}			2.95		μC

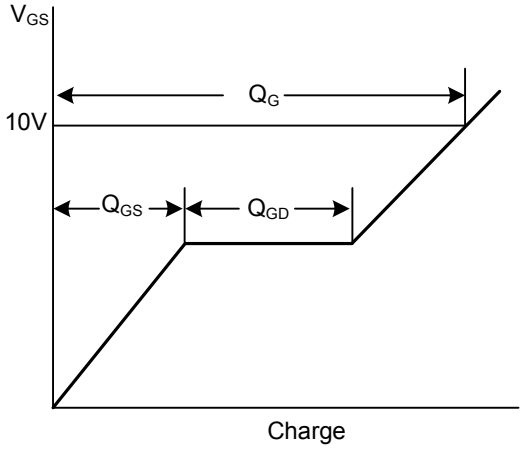
Notes: 1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. L = 8mH, I_{AS} = 9A, V_{DD} = 50V, R_G = 25Ω, Starting T_J = 25°C
3. I_{SD} ≤ 9A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C
4. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%
5. Essentially independent of operating temperature

■ TEST CIRCUITS AND WAVEFORMS

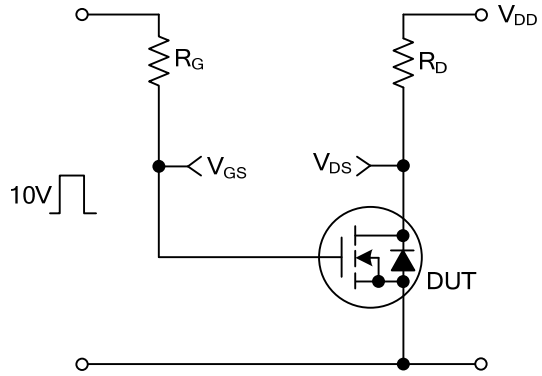
Gate Charge Test Circuit



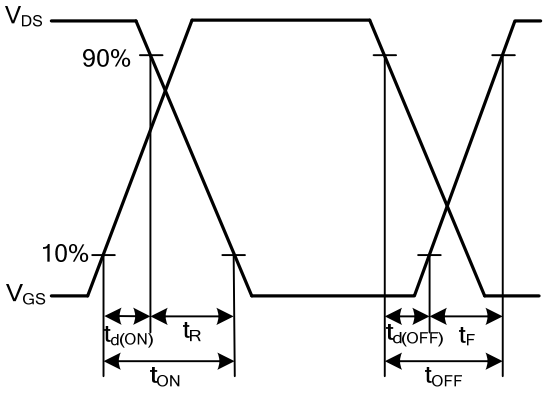
Gate Charge Waveforms



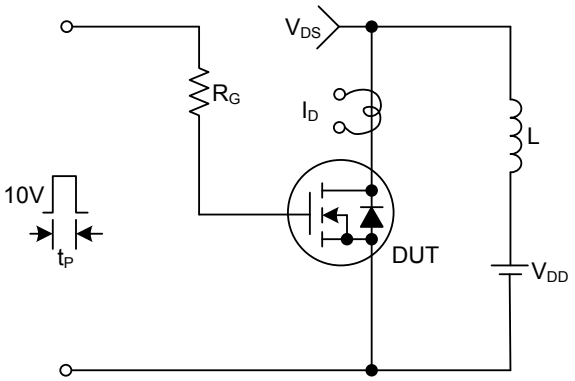
Resistive Switching Test Circuit



Resistive Switching Waveforms

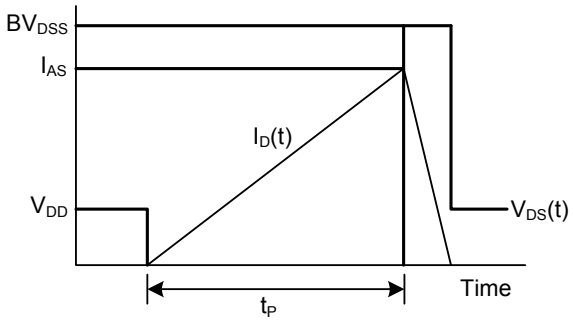


Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

$$E_{AS}=\frac{1}{2}LI_{AS}^{2}\frac{BV_{DSS}}{BV_{DSS}-V_{DD}}$$



Peak Diode Recovery dv/dt Test Circuit & Waveforms